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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

MAY 05 2006

Group Art Unit: 2891  
Examiner: Asok K. Sarkar  
Confirmation No.: 7861

## In Re PATENT APPLICATION OF:

Applicant: Masaru Takaishi

Serial No.: 10/809,682

Filed: March 26, 2004

For: SEMICONDUCTOR DEVICE  
WITH LOW RESISTANCE REGION

Attny Ref.: AI 332

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May 5, 2006Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This paper is in response to the Official Action mailed on December 6, 2005. Payment for two months' extension of time is attached. Please charge our Deposit Account No. 18-0002 if any additional fees are needed to enter this paper, and please advise us accordingly. It is noted that no petition is required because of the authorization to charge, but this paper is a petition for extension of time.

Non-elected claims 1-5 are canceled without prejudice to reentry. Claim 9 is combined with claim 6, so that the subject matter of claim 6 is now exactly the subject matter of previous claim 9, and no new issue is raised. The combination of claims 6 and 9 is argued for below. Claim 7 is amended formally and for clarity as the Examiner suggested.

New claims 14-15 are supported in the text spanning pages 20-21, and page 19, lines 12-15, respectively. The new claims are patentable for the reasons below.

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